

ISR-100/200 Series Operations Manual

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ISR Series Operations Manual

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Part



1 Wiegand Protocol

1.1 Specification

Specification

CPU	: 8bit RISC Micro Processor
Memory	: Program Memory(16, 4 KByte)
Power	: DC 12V/ 300mA max.
Reading Range	: 5~ 10Cm
Communication	: Wiegand (RS-232)
Reader Type	: EM / HID Card Reader
Reset	: Power on reset & watch dog timer
LED	: 2 LEDs (Red, Green) Control Signal Input : 1 LED (Card Reading Status or Program

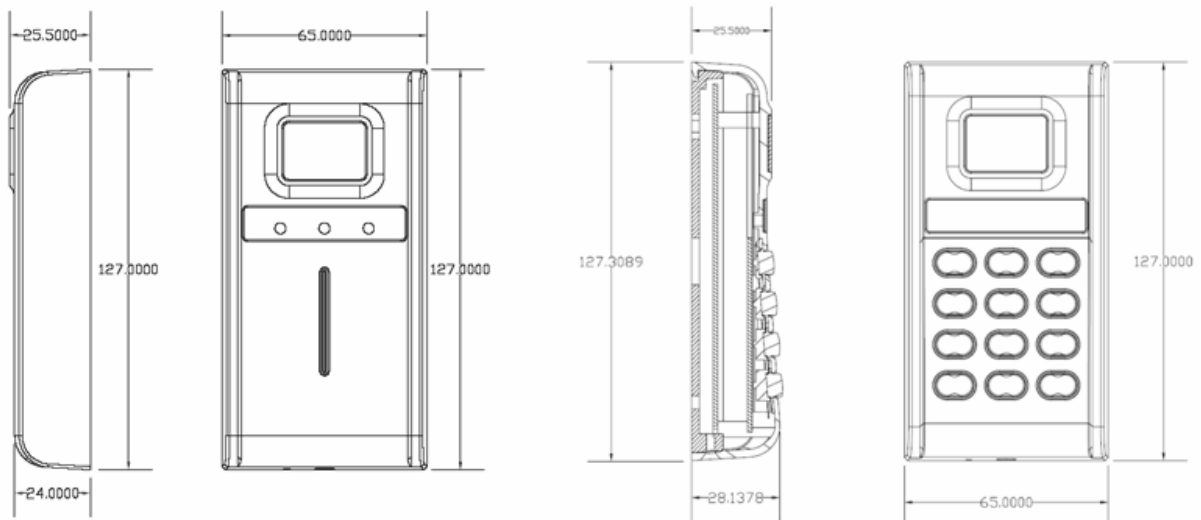
Status)

Buzzer	: Buzzer Control Signal Input
Environment	: -10° C to +65° C, 10% ~ 90%
Weight	:
Dimension	:
Color	: Silver
Material	: ABS

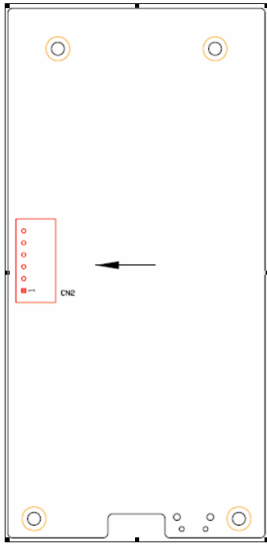
Card Reader Specification

Antenna Type	:Coil
Frequency	:125 KHz / 13.56MHz
Radio Frequency Type	:A1D
Communication Type	:Single Type
Modulation Type	:Amplitude Modulation
Antenna Dimension	:45x55mm
Antenna Connection Interface	:Fixed

1.2 ISC-100/200 SIZE



1.3 Wiegand Connector



CN2

Pin1 : VCC (RED)

Pin2 : GND (Black)

Pin3 : DATA0 (White) / RS-232 RX

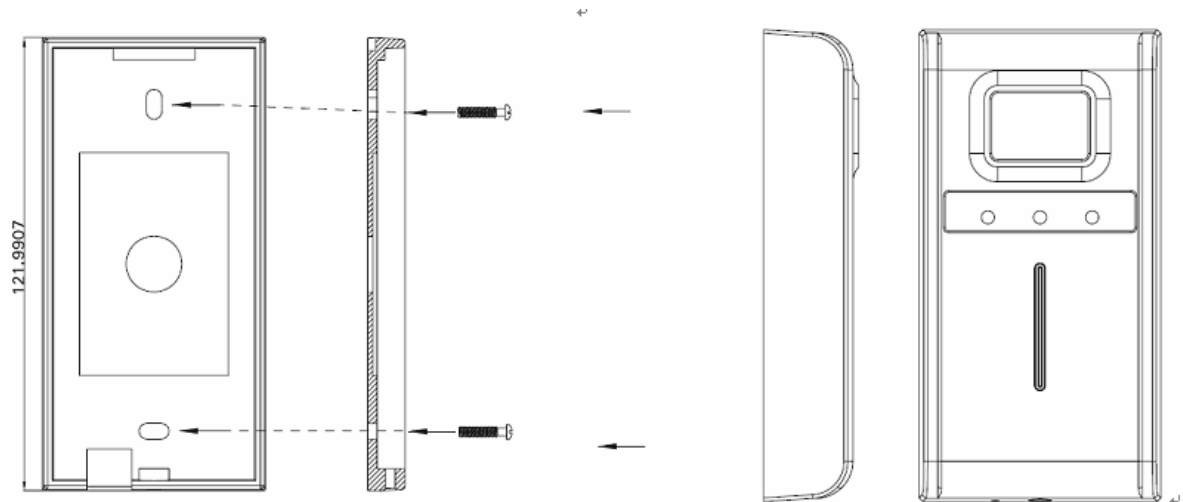
Pin4 : DATA1 (Green) / RS-232 TX

Pin5 : Green LED Control (Blue)

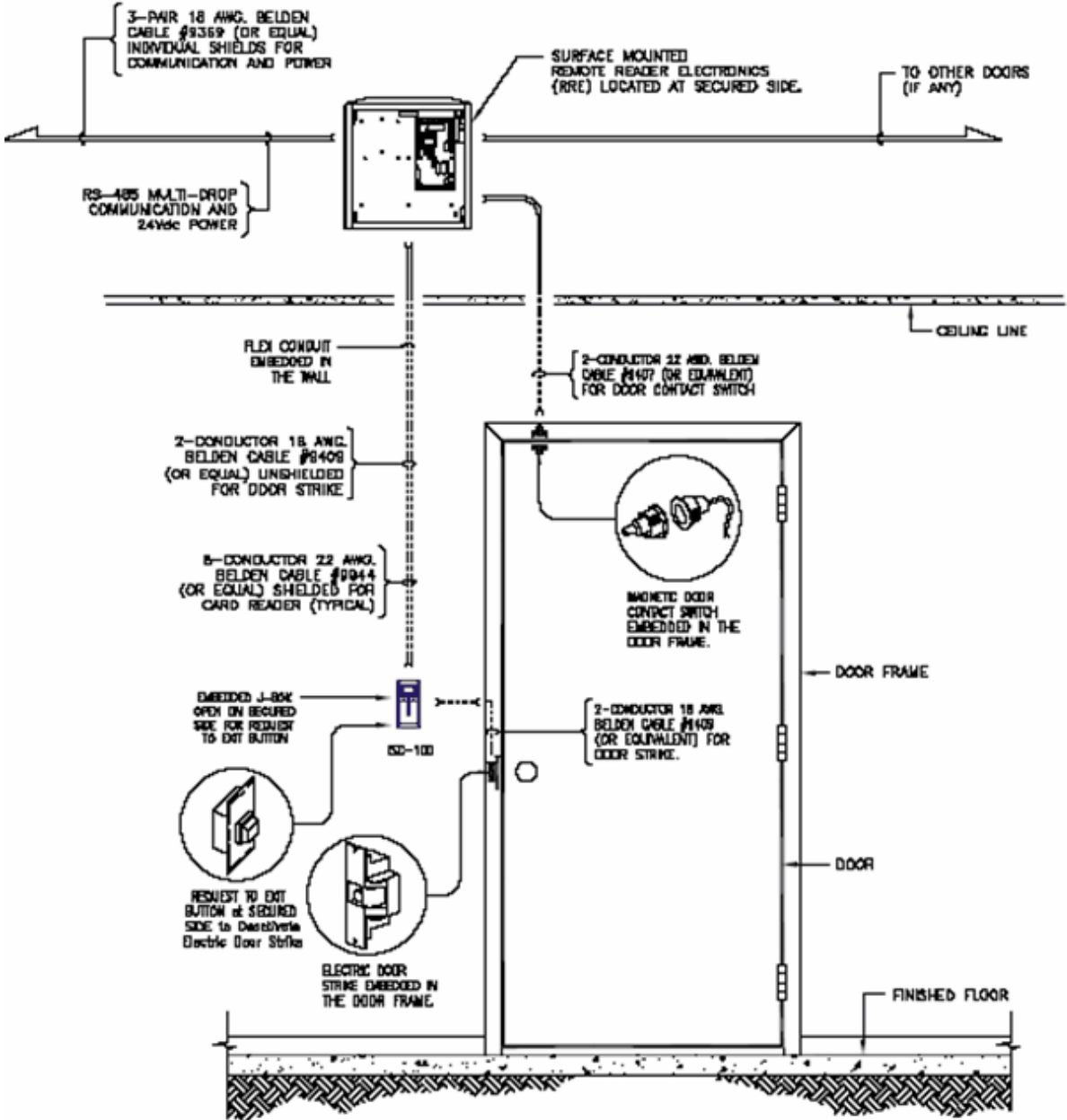
Pin6 : Red LED Control (Gray)

Pin7 : Buzzer Control (Yellow)

1.4 Mounting



1.6 Installation Overview



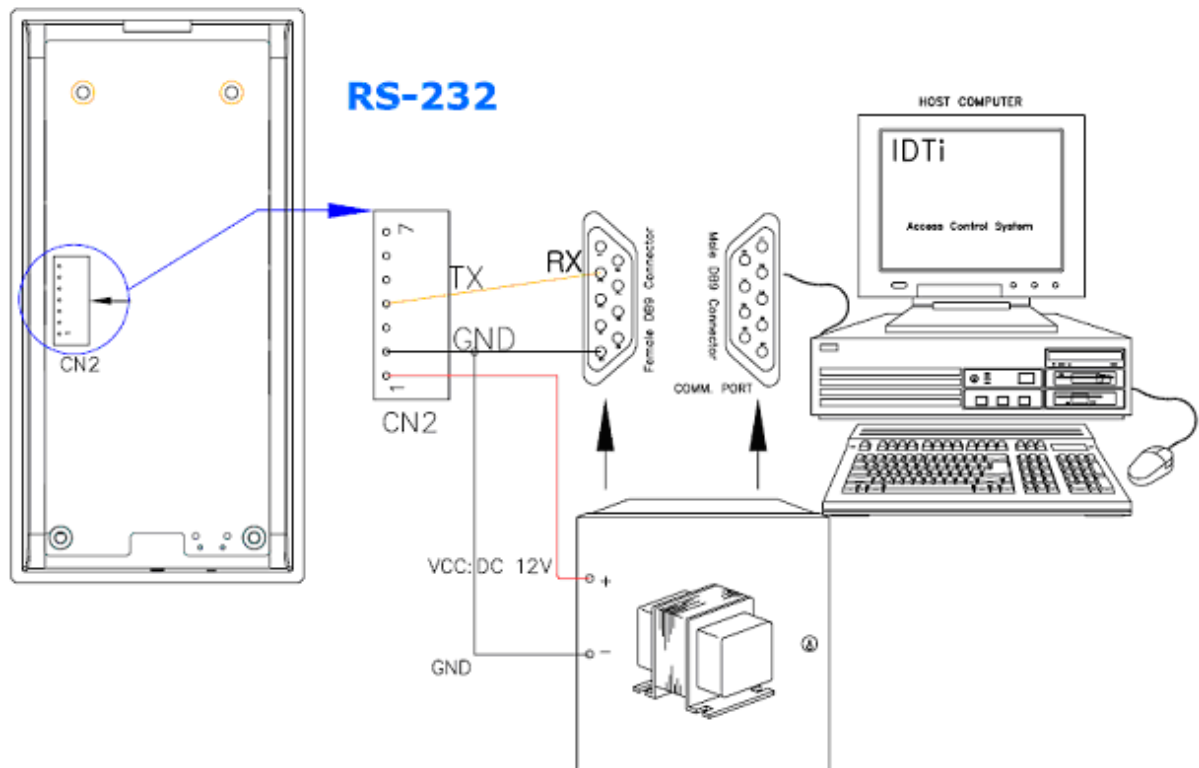
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Part



2 RS 232 Protocol



2.1 Card Data

STX	LEN	F2	F1	F0	D5	D4	D3	D2	D1	D0	DCS	ETX
0x02	0x0D											0x03

Facility Code : F2~F0
 Ex> If 125
 F2:0x01

F1:0x02
F0:0x05

Card Number : D5~D0

Ex> If 65321
D5:0x00
D4:0x06
D3:0x05
D2:0x03
D1:0x02
D0:0x01

Data Checksum : $DCS = F2+F1+F0+D5+D4+D3+D2+D1+D0$
 $0x01 + 0x02 + 0x05 + 0x00 + 0x06 + 0x05 + 0x03 + 0x02 + 0x01 = 0x19$

STX	LEN	F2	F1	F0	D5	D4	D3	D2	D1	D0	DCS	ETX
0x02	0x0D	0x01	0x02	0x05	0x00	0x06	0x05	0x03	0x02	0x01	0x19	0x03

2.2 Key Data

Up to 16 digits can be used.
To clear, press '*'; to transmit data, press '#'

STX	LEN	D7	D6	D5	D4	D3	D2	D1	D0	DCS	ETX
0x02	0x0C										0x03

Key Number : D7~D0

Ex> if 1234567890
D7:0x00
D6:0x00
D5:0x00
D4:0x12
D3:0x34
D2:0x56
D1:0x78
D0:0x90

Data Checksum : $DCS = D7 + D5 + \dots + D1 + D0$
 $0x00 + 0x00 + 0x00 + 0x12 + 0x34 + 0x56 + 0x78 + 0x90 = 0xA4$

STX	LEN	D7	D6	D5	D4	D3	D2	D1	D0	DCS	ETX
0x02	0x0C	0x00	0x00	0x00	0x12	0x34	0x56	0x78	0x90	0xA4	0x03